



Introduction to Micro/Nano Fabrication Techniques

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Fabrication of Nanomaterials

- **Top-Down Approach**
 - Begin with bulk materials that are reduced into nano-scale materials
 - Ex: Traditional Machining
- **Bottom-Up Approach**
 - Begin with atoms and molecules that can grow into zero, one, two, and three-dimensional nanostructures
 - Ex: Chemical Synthesis
- **Hybrid**
 - Top-Down + Bottom-Up



Top-Down Approach

- Mechanical energy
 - Ball milling, polishing, grinding
- Thermal
 - Annealing, evaporation, pyrolysis
- High energy
 - Arch, laser, ion milling, reactive ion etching
- Chemical
 - Chemical etching, CMP, electropolishing, anodizing
- Lithographic
 - Photo, e-beam, EUV, X-ray, μ -cp, NIL, Nanosphere
- Nature
 - Erosion, decomposition, digestion



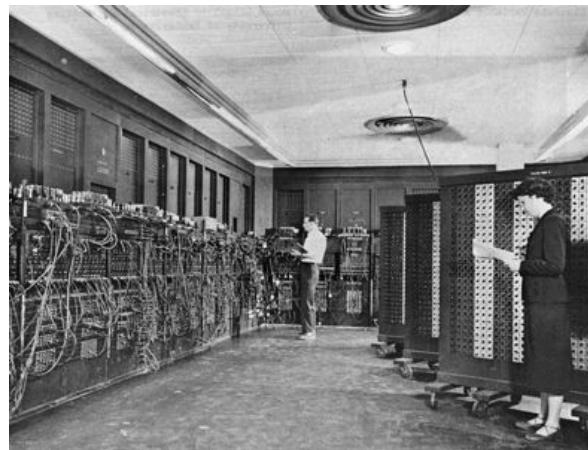
Bottom-Up Approach

- Gas
 - Chemical vapor deposition, atomic layer deposition, MOCVD, MBE, ion implantation
- Liquid
 - Self-assembly, supermolecule, reduction, template synthesis
- Lithographic
 - Dip-pen, block co-polymer, STM writing
- Biological
 - Protein, nuclear acidcrystal formation





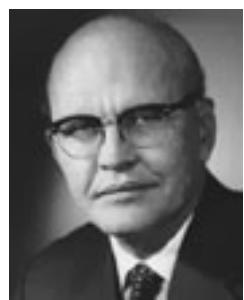
Building a Computer



ENIAC: Electronic Numerical Integrator And Computer, 1946.

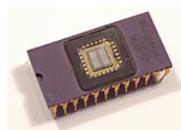


First Integrated Circuit



"What we didn't realize then was that the integrated circuit would reduce the cost of electronic functions by a factor of a million to one, nothing had ever done that for anything before" - Jack Kilby 2000 Nobel Prize

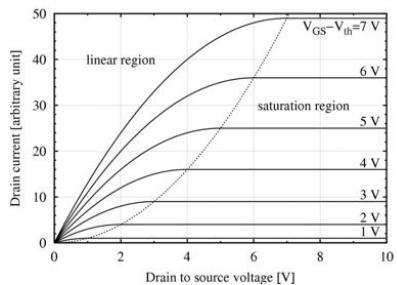
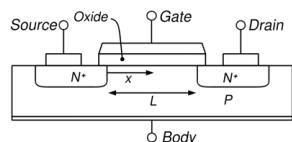
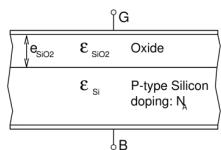
1958 Texas Instruments





MOSFET

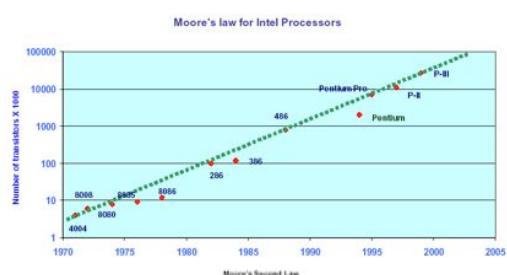
- Metal-Oxide-Semiconductor Field-Effect Transistor



- Small Print
- Low Power Dissipation
- Batch Process
- Fast Response
- Pure Electrical Switch, No Moving Parts



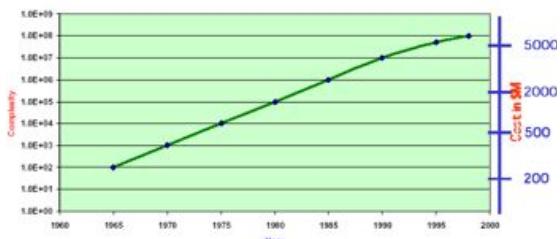
Moore's Law



Moore's Laws

First Law: Number of components in a chip (IC) will double roughly every 18 months (1965, in [Electronics](#)). This has held true more or less since then.

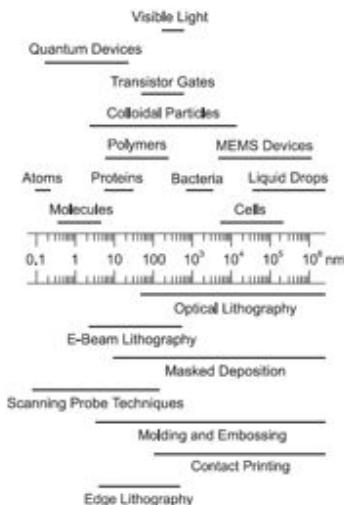
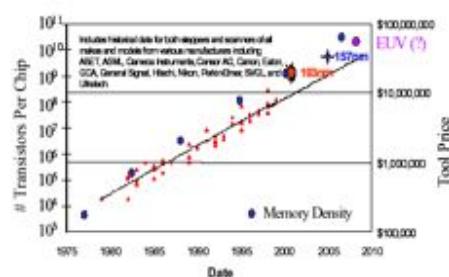
Second Law: Facility costs increase on a semilog scale (terminology due to Eugene Meieran, Intel Fellow). Fab costs double approximately every four years.





Tool Cost

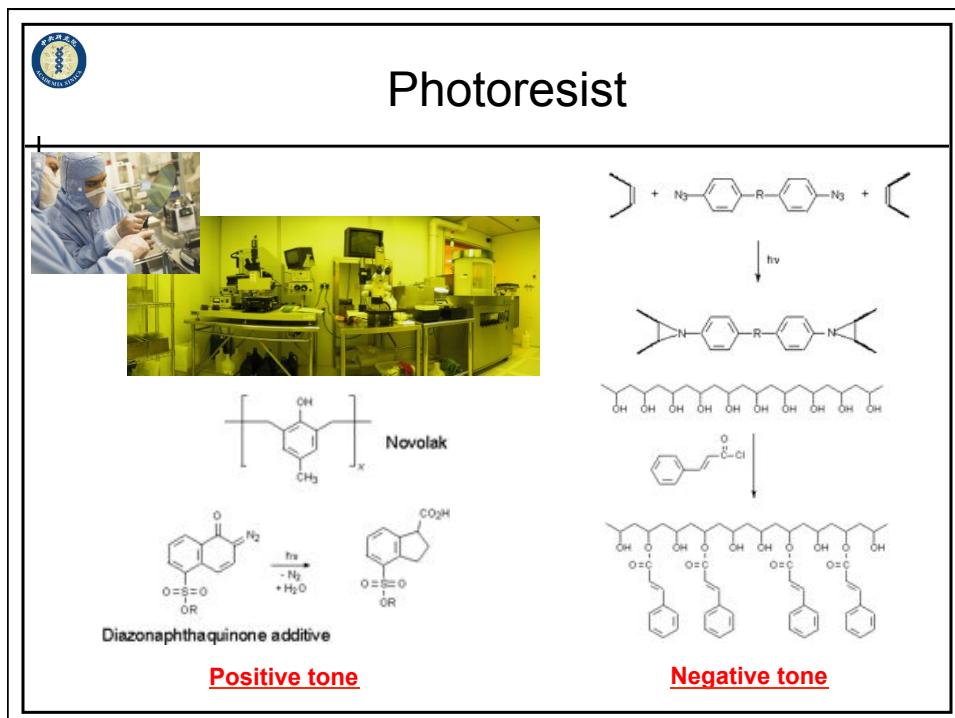
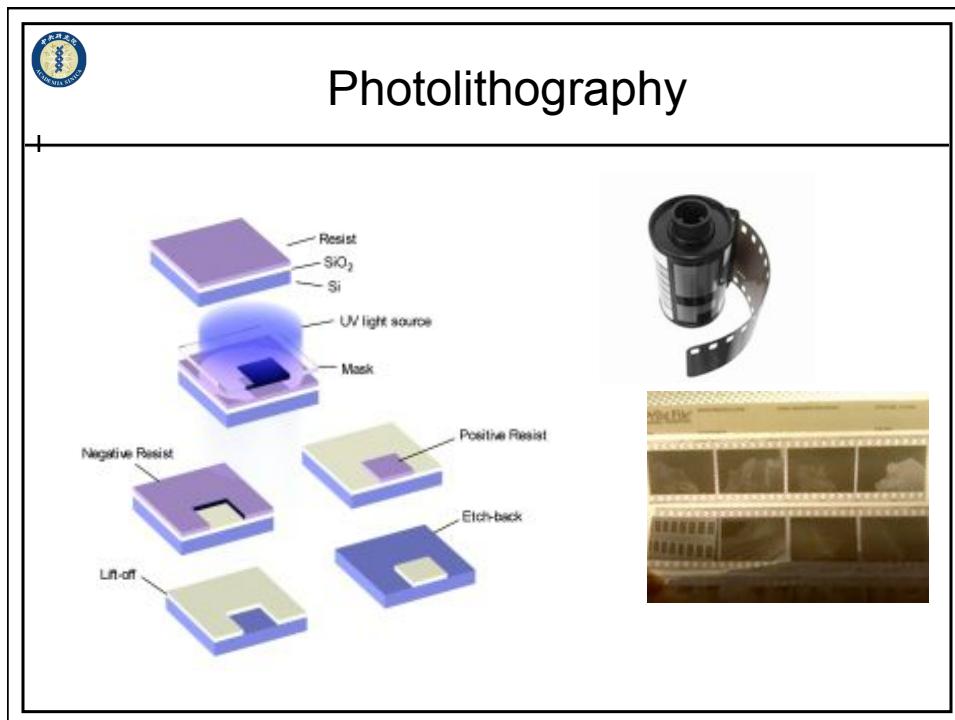
- Why does the tool cost increase so fast?
- What is the bottleneck?



Industrial Process

- Lithography
- Deposition
- Etching
- Planization
- Packaging







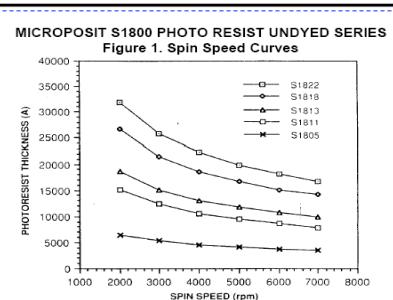
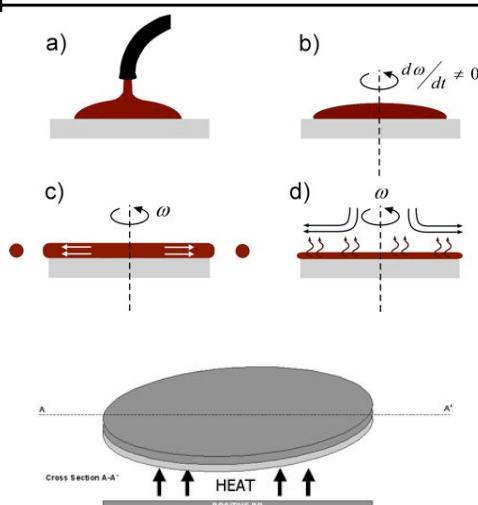
STEP I: Cleaning

RCA Cleaning (By Radio Corporation of America in 1965)

Chemicals	Volume ratio	Procedure Time (min)	Operation Temperature	Function
Trichloroethane		5	Room T	Dissolve Organic
Acetone		5	Room T	Dissolve Organic
DI Water		5	Room T	Washing
H_2SO_4 (98%)– H_2O_2 (30%) (Piranha Solution)	3:1	10-20	$\sim 90^\circ\text{C}$	Oxide and Dissolve Organic and Metals
DI Water		5	Room T	Washing
HF(49 wt %)– H_2O	$\sim 2:100$	10-20	Room T	Dissolve surface SiO_2
NH_4OH (29%)- H_2O_2 (30%)- H_2O	1:1:5	10-20	$\sim 90^\circ\text{C}$	Oxide and Dissolve Metals
DI Water		5	Room T	Washing
HCl (37%)- H_2O_2 (30%)- H_2O	1:1:5	10-20	$\sim 90^\circ\text{C}$	Oxide and Dissolve Metals
DI Water		5	Room T	Washing
Spin Dry (In lad – N_2 blow)				



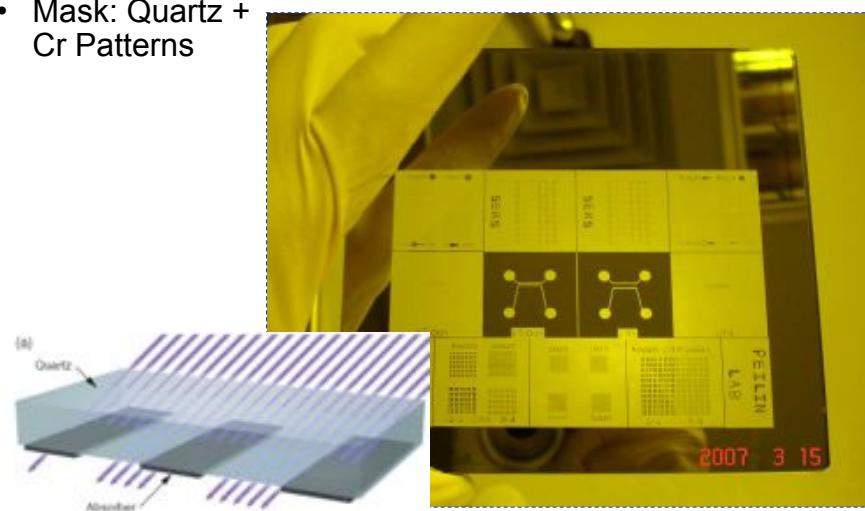
STEP II: PR Spin Coating/Soft Bake



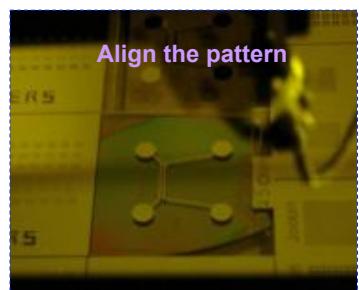
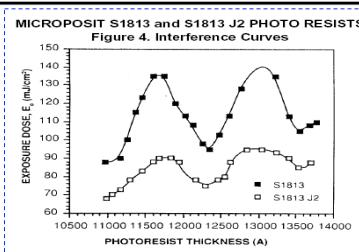


STEP III: Align and Exposure

- Mask: Quartz + Cr Patterns

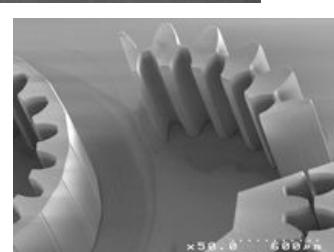
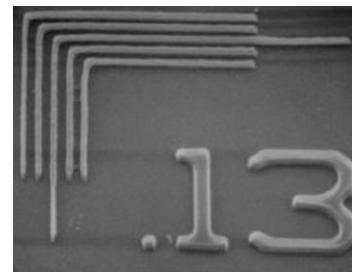


STEP III: Align and Exposure

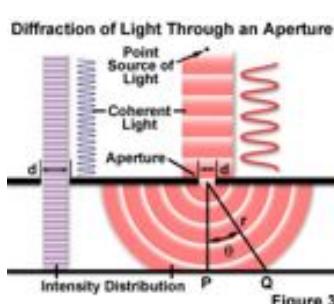




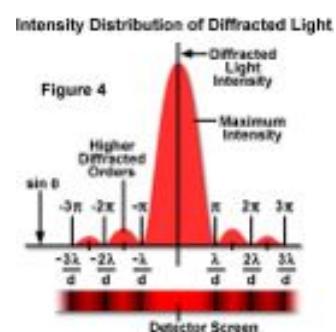
STEP IV: (PEB) and Develop



Limitation of Optical Lithography

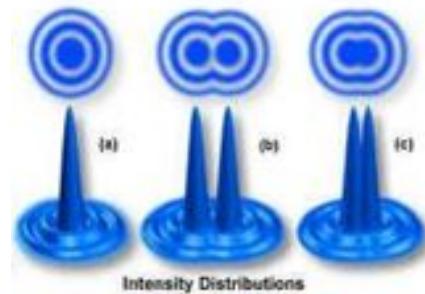


$$r = 1.22 \times \lambda / (2 \times \text{N.A.})$$
$$\text{N.A.} = n \times \sin(\theta)$$





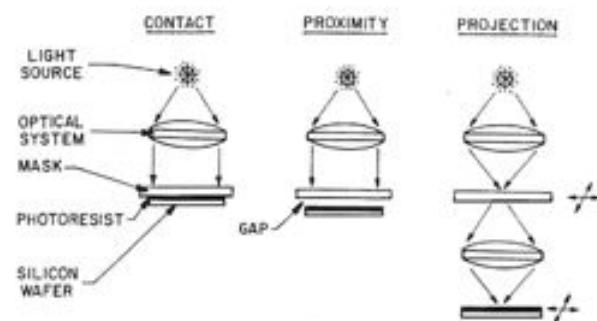
Diffraction Limit



$$\text{Resolution} = K \times \lambda / (\text{N.A.})$$
$$\text{Depth of Focus} = \lambda / (\text{N.A.})^2$$
$$K = 0.61$$



Photolithography Types



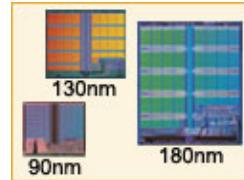
$$W_{\min} = k_1 \sqrt{\lambda d}$$

$$W_{\min} = k_1 \lambda / \text{NA}$$

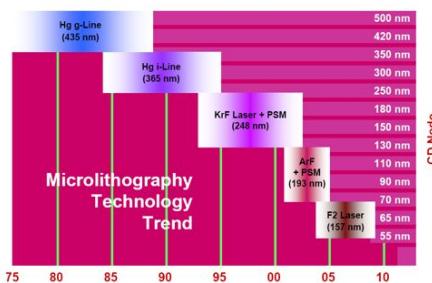


Photolithography Types

UV Wavelength (nm)	Wavelength Name	UV Emission Source
436	g-line	Mercury arc lamp
405	h-line	Mercury arc lamp
365	i-line	Mercury arc lamp
248	Deep UV (DUV)	Mercury arc lamp or Krypton Fluoride (KrF) excimer laser
193	Deep UV (DUV)	Argon Fluoride (ArF) excimer laser
157	Vacuum UV (VUV)	Fluorine (F ₂) excimer laser



Year	Linewidth (nm)	Wavelength (nm)
1986	1,200	436
1988	800	436/365
1991	500	365
1994	350	365/248
1997	250	248
1999	180	248
2001	130	248
2003	90	248/193
2005 (fcst)	65	193
2007 (fcst)	45	193



Water Immersion Lithography

Year	Linewidth (nm)	Wavelength (nm)
1986	1200	436 g-line mercury lamp
1988	800	436/365
1991	500	365 i-line mercury lamp
1994	350	365/248
1997	250	248 KrF excimer laser
1999	180	248
2001	130	248
2003	90	248/193
2005	65	193 ArF excimer laser
2007	45	193/157

$$\text{Resolution (R)} = K \times \lambda / (\text{N.A.})$$

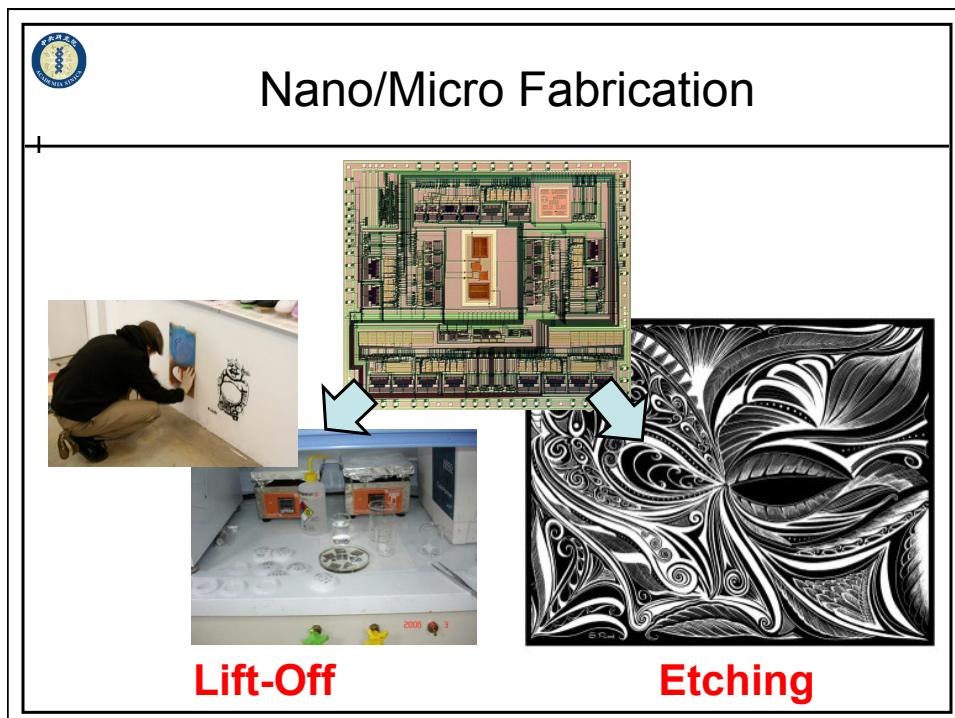
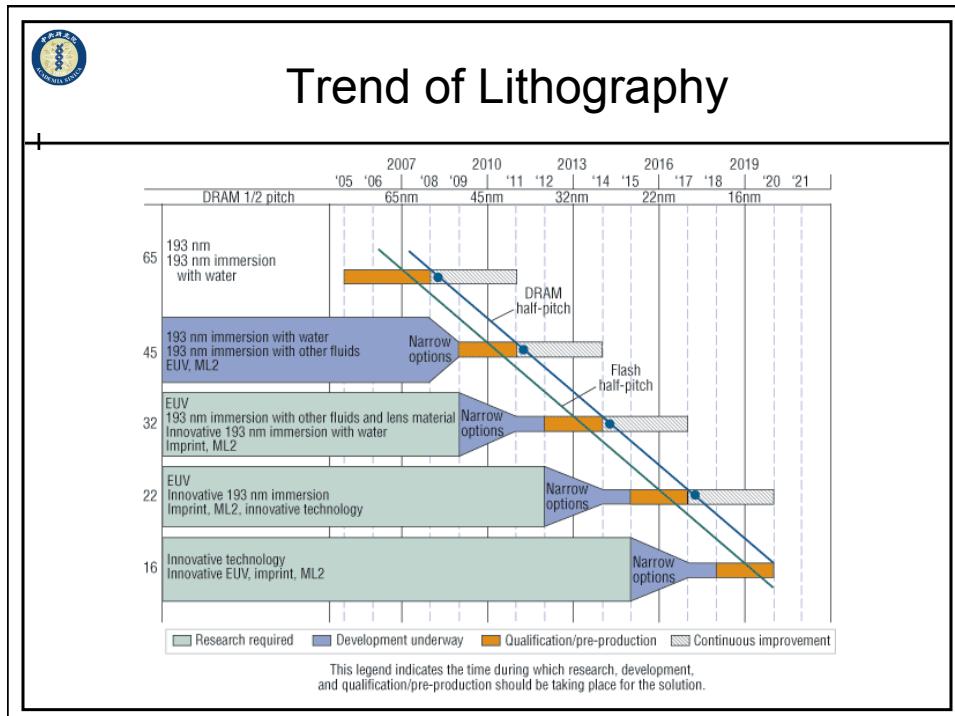
$$K = 0.25, \text{NA} \sim 1.4, \lambda = 193$$

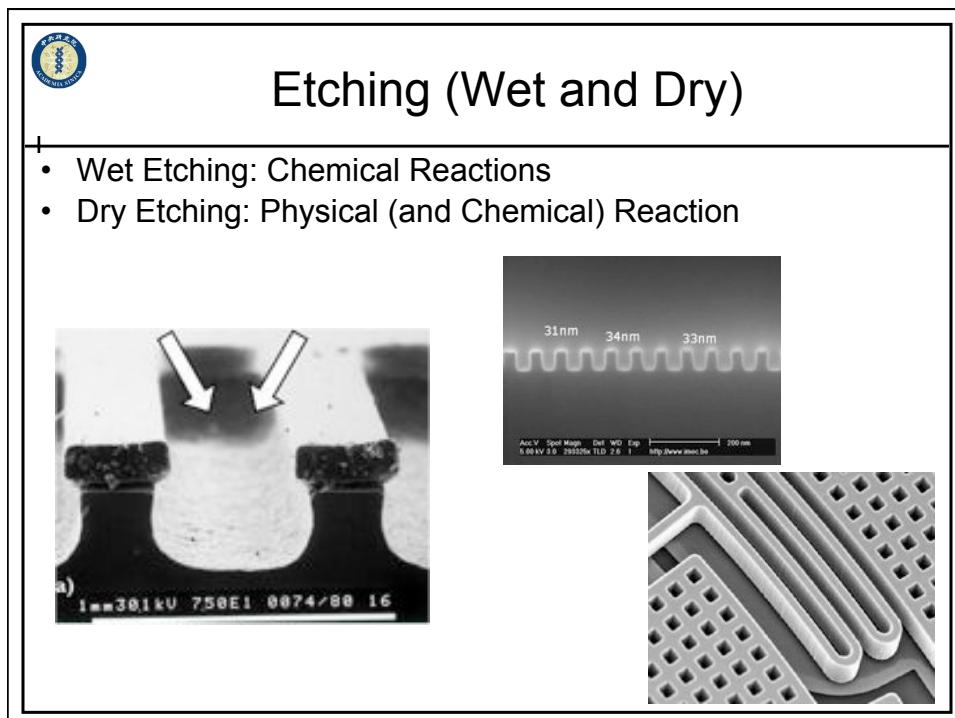
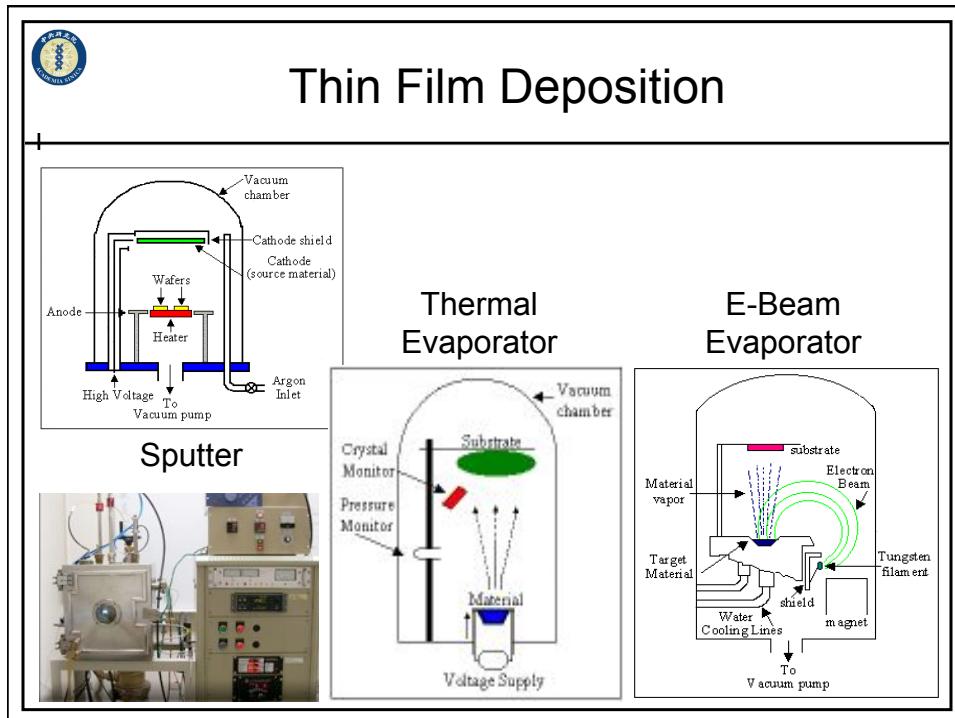
$$R = 35 \text{ nm}$$

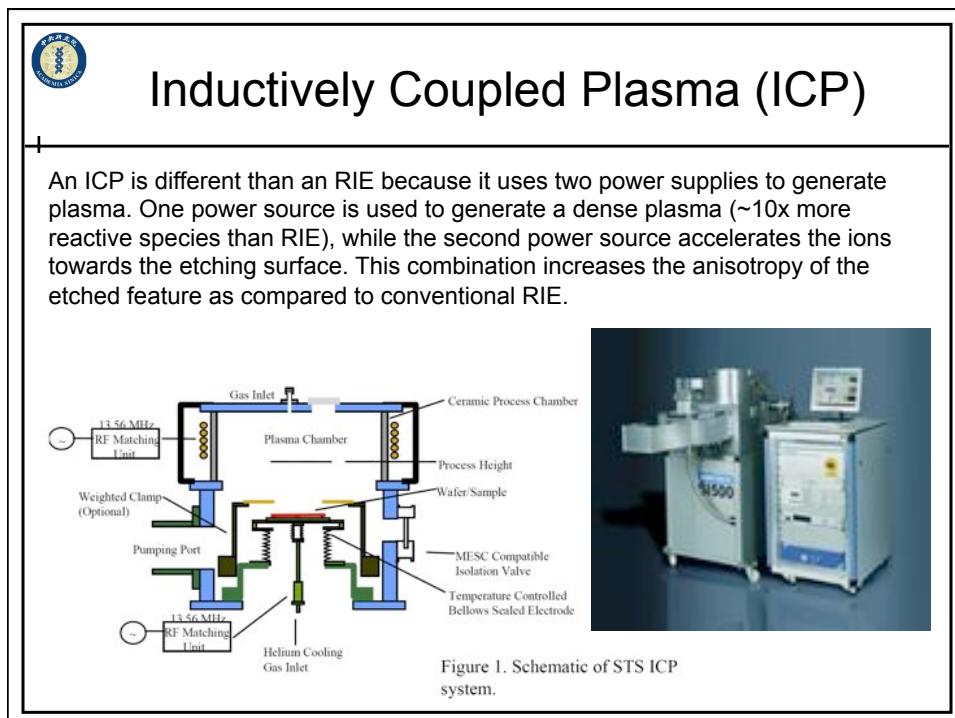
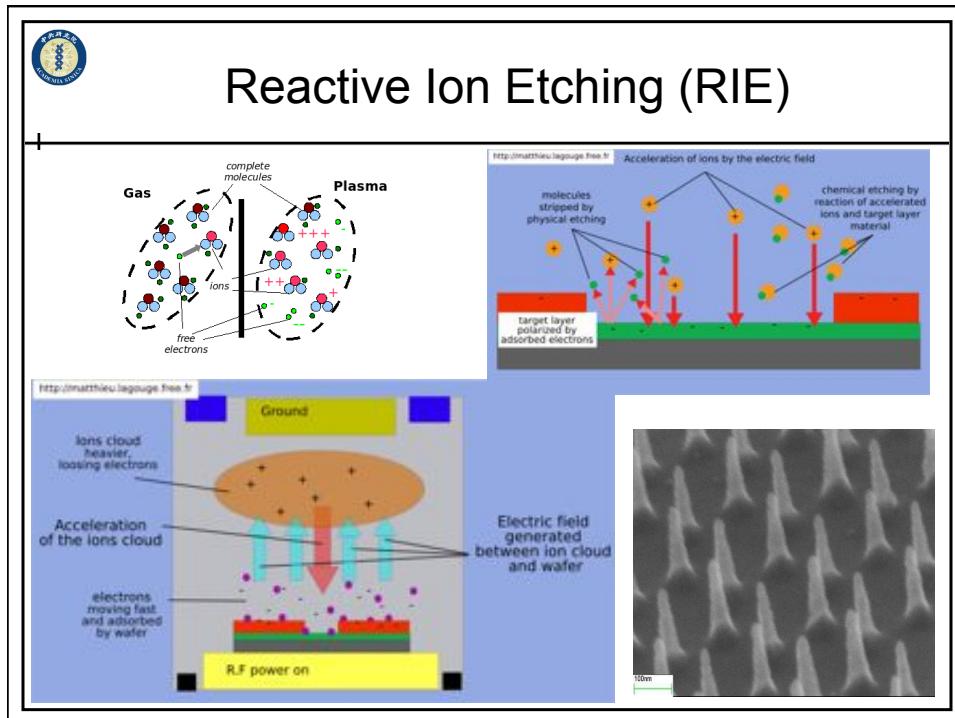
$$\text{Air } n = 1.0003$$

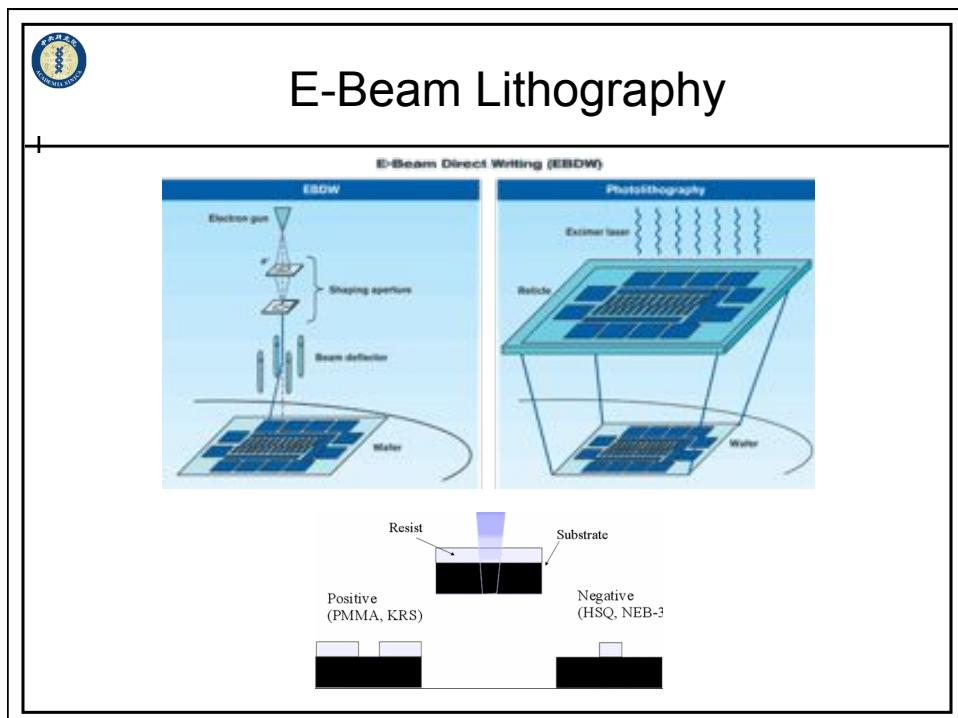
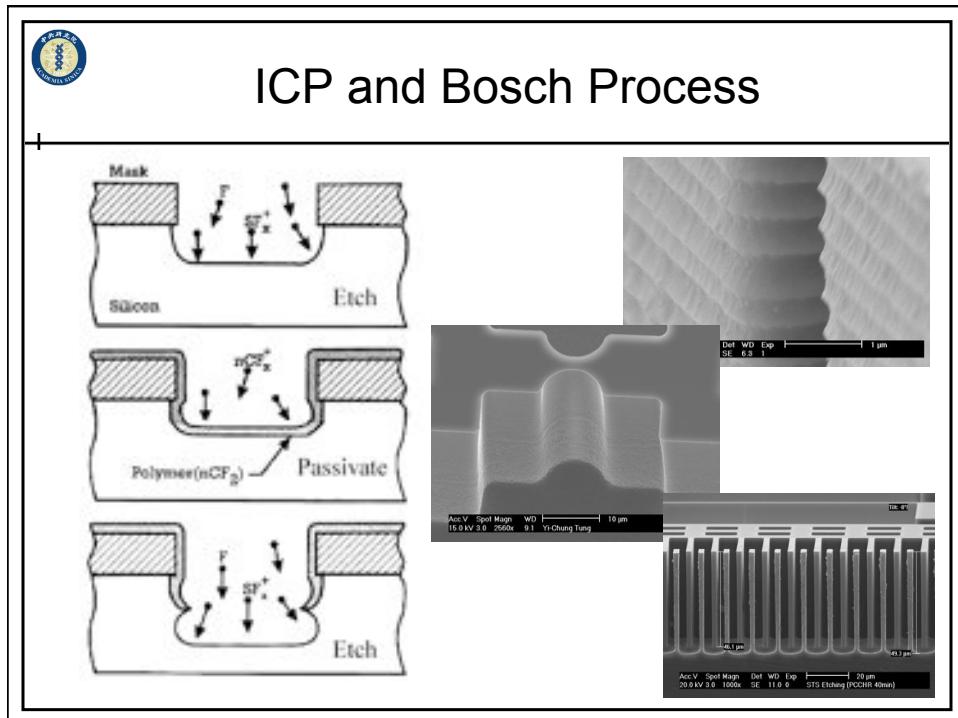
$$\text{Water } n = 1.437$$

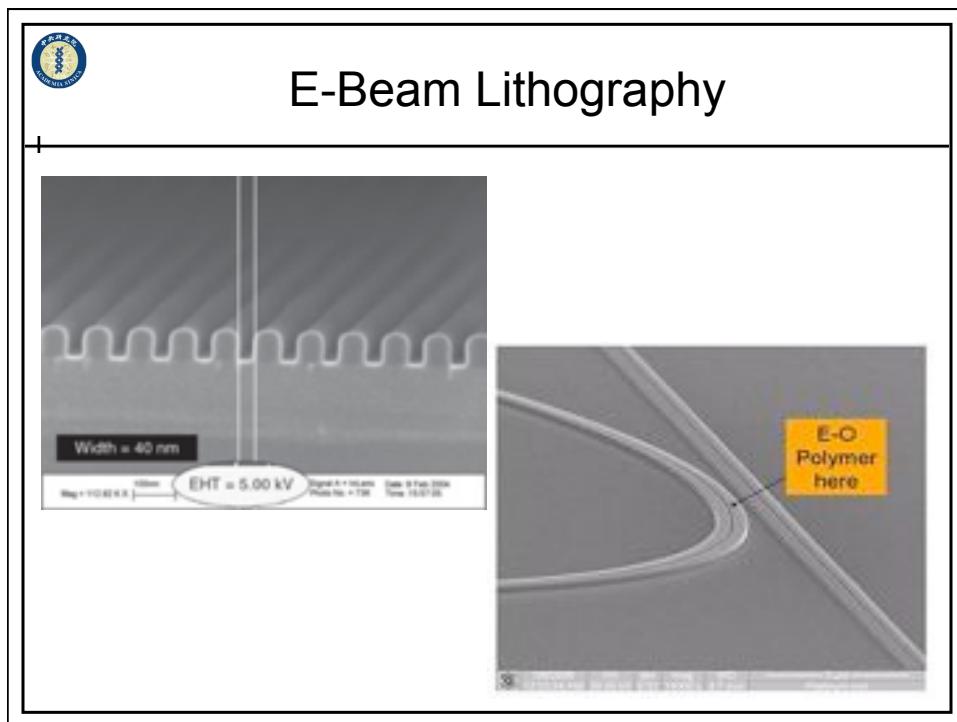
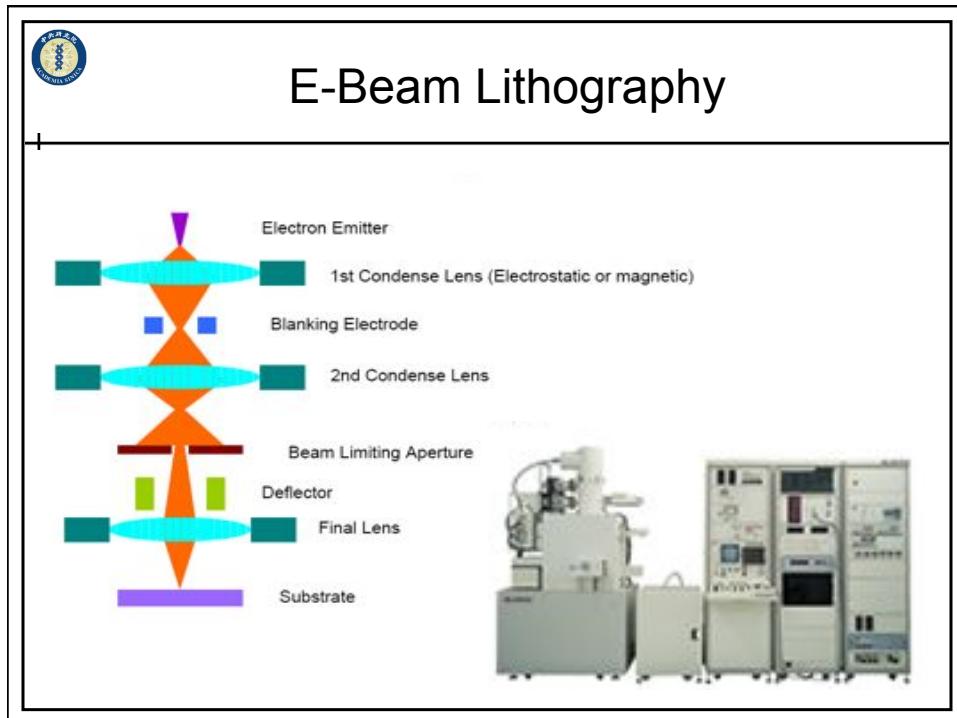
The resolution is increased by a factor equal to the refractive index of the liquid. Current immersion lithography tools use highly purified water for this liquid, achieving feature sizes below 45 nanometers.













Nanoimprint Lithography (NIL)

Mold

PMMA

Substrate

Imprint

Remove Mold

RIE

Evaporation

Lift-off (or Etching)

→ | ← 10 nm

082961 5.0K X250K 120ns

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Nanoimprint Lithography (NIL)

Ultrafast and direct imprint of nanostructures in silicon

Stephen Y. Chou*, Chris Keimel & Jian Gu

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a Contact mould and substrate ($t = 0$)
Quartz (green)
Si (blue)

b Excimer laser irradiation ($t > 0$)
Molten Si (red)

c Silicon embossing ($0 < t < 250$ ns)
Silicon solidification ($t > 250$ ns)

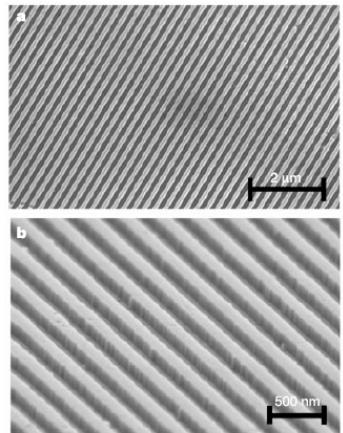
d Mould and substrate separation

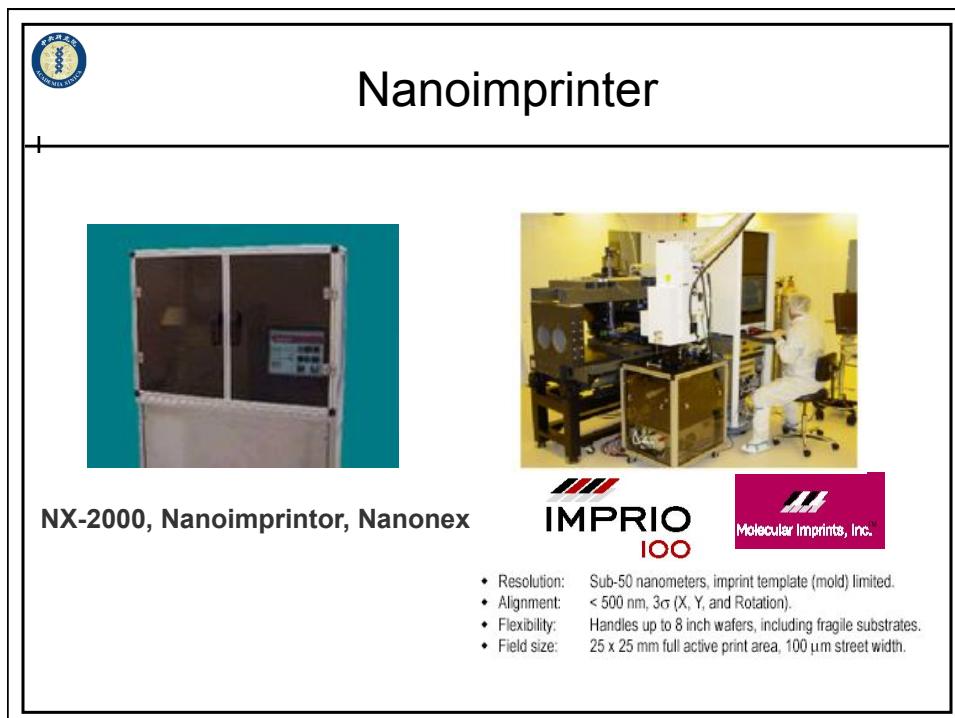
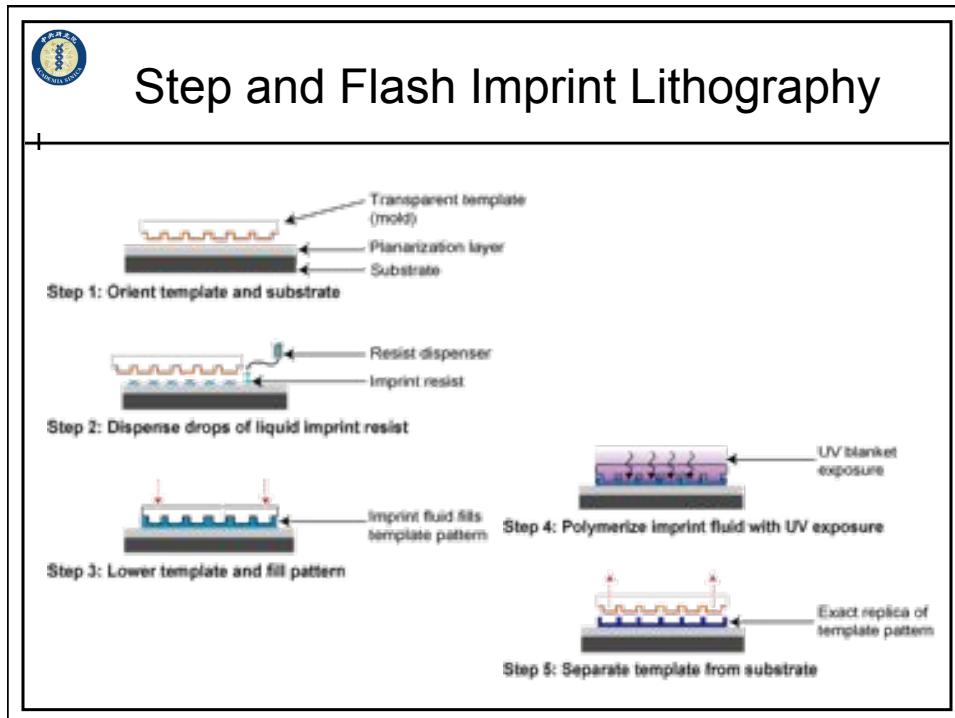
e

f Reflectivity (a.u.) vs Time (ns)

2 μm

500 nm

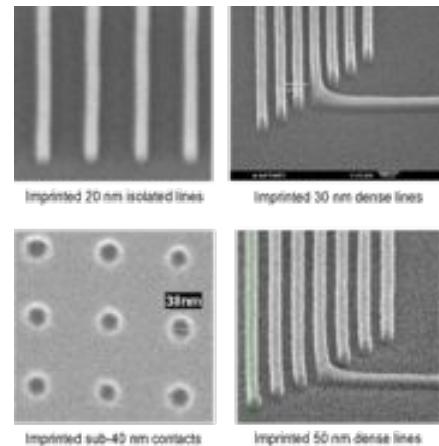






Nanoimprint Results and Challenge

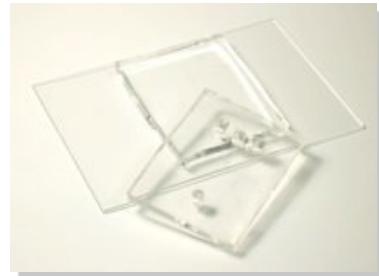
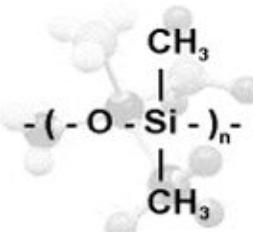
- Mask Fabrication (1:1)
- Lift-off process
- Resist
- Mask Design





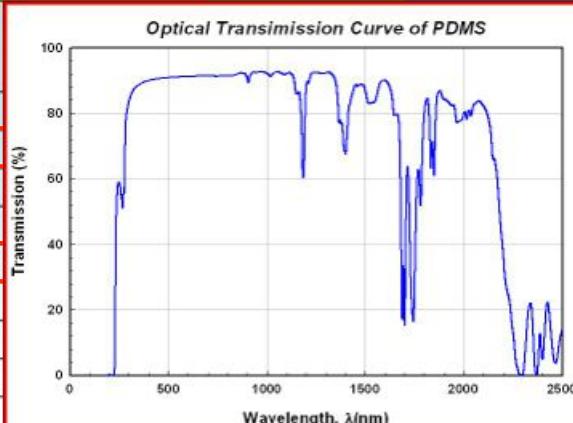
Soft Lithography - PDMS

- PDMS (Polydimethylsiloxane)
 - PDMS is durable, optically transparent, and inexpensive
 - PDMS can be patterned by *Soft Lithography*

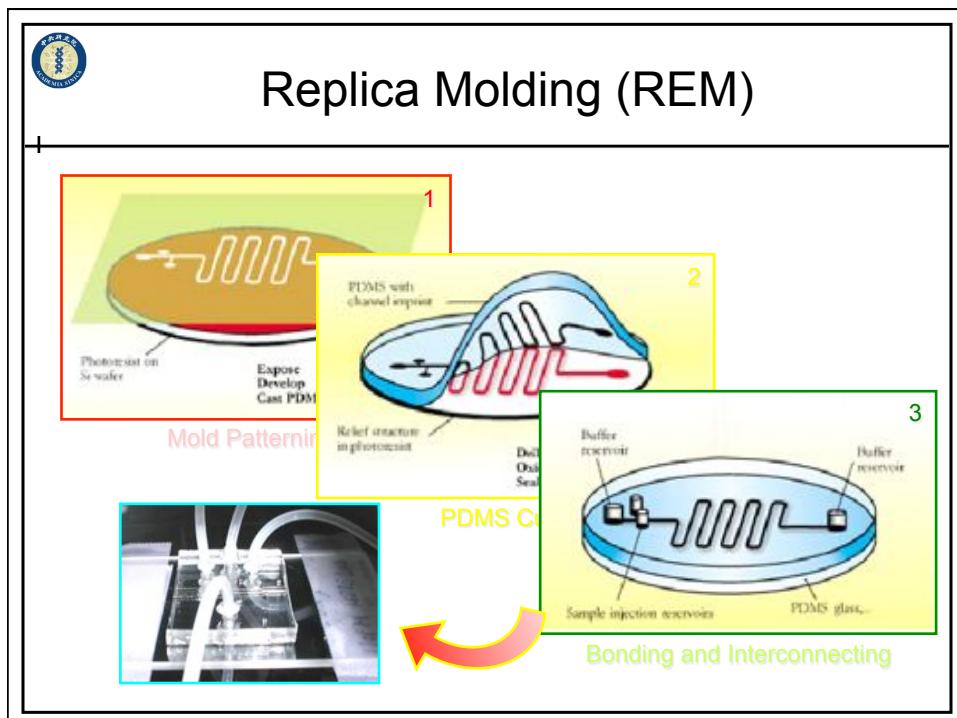
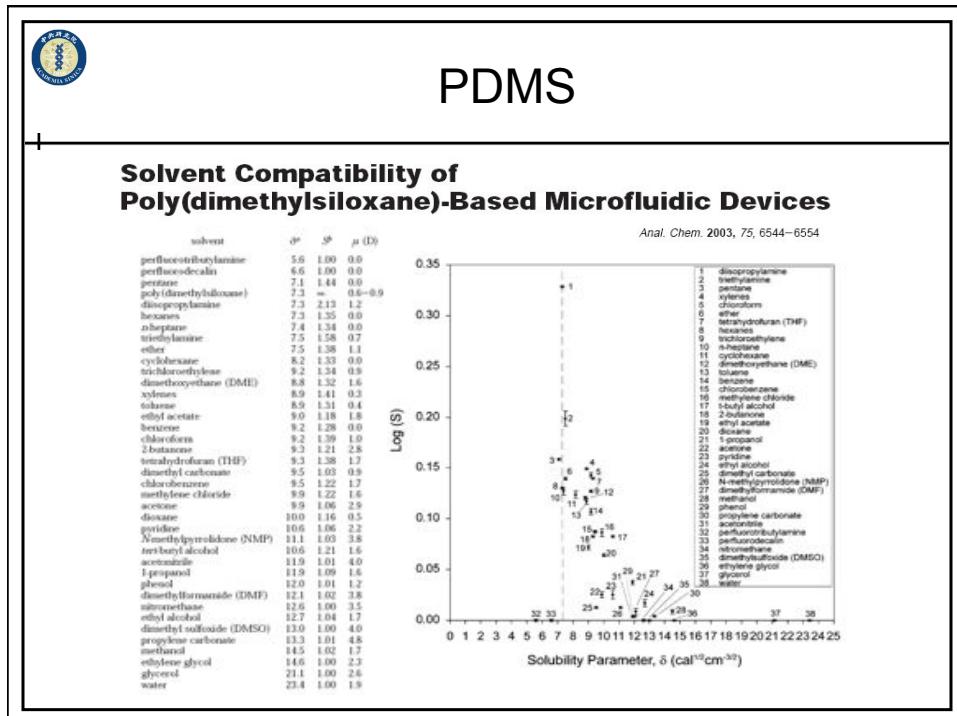


PDMS Material Properties

Density
Young's Modulus
Poisson's Ratio
Tensile Strength
Maximum Strain
Thermal Expansion Ratio
Thermal Conductivity
Permittivity
Resistivity
Transparency (Visible Light)



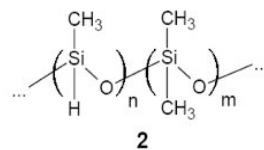
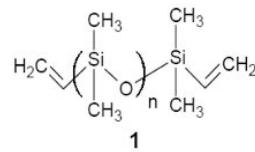
Very Good Excellent Opaque



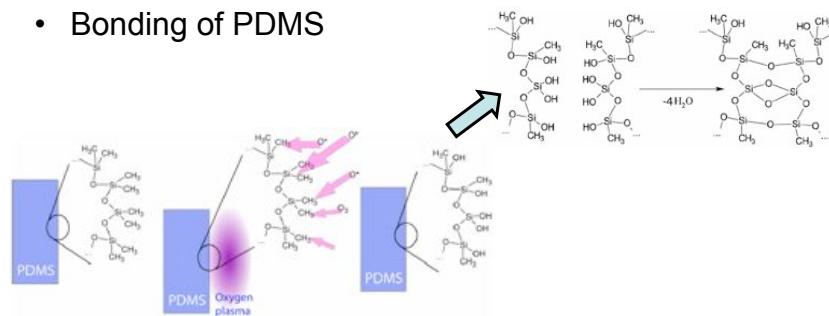


PDMS Curing and Bonding

- Curing of PDMS



- Bonding of PDMS



Micro Contact Printing (μ CP)

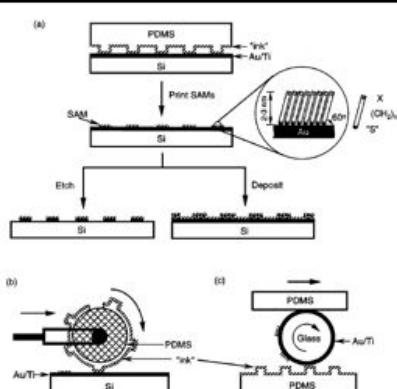
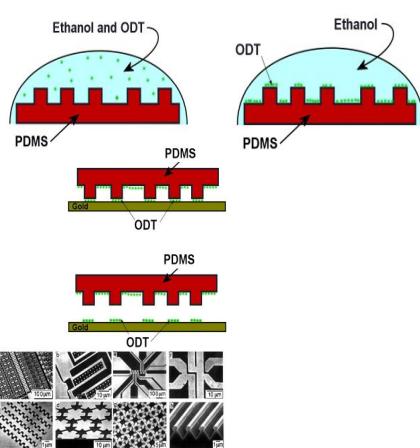
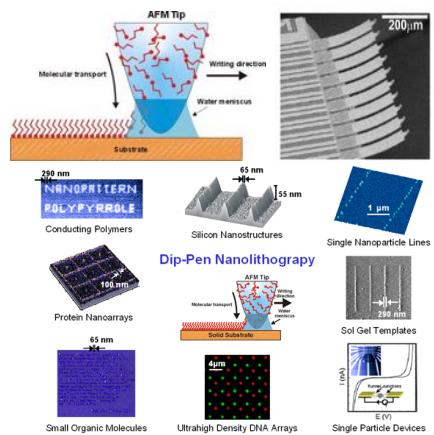
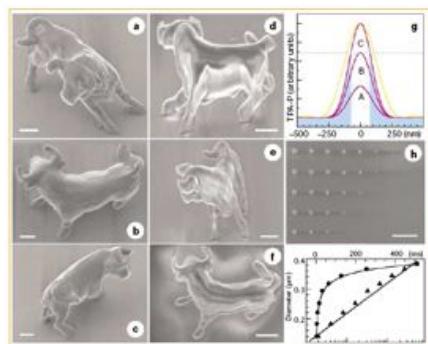


Figure 2 Schematic procedures for μ CP of hexadecanethiol (HDT) on the surface of gold: (a) printing on a planar surface with a planar stamp (21), (b) printing on a planar surface over large areas with a rolling stamp (128), and (c) printing on a nonplanar surface with a planar stamp (174).



Others



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